

In addition to resistors that we have considered to date, there are two other basic electronic components that can be found everywhere: the capacitor and the inductor. We will consider these two types of components in this lecture and also in Lab Experiment 2.



Besides resistors, capacitors are one of the most common electronic components that you will encounter. Sometimes capacitors are components that one would deliberately add to a circuit. Other times, capacitors are side effects that come about even if we don't want them.

The simplest capacitor is formed by an insulating material (known as dielectric) sandwiched between two parallel conducting plates. When a voltage potential is applied to the two ends, charge accumulates on the plates.

In capacitors, voltage v is proportional to the charged stored q. The constant of proportionality is the capacitance C. Since current is the rate of charge of charge (i.e. the flow of charge), the relationship between v and l involves differentiation or integration.

Capacitance is measured in Farads.



Since there is an insulating layer between the two conducting plates of a capacitor, DC current cannot flow through a capacitor. So always remember: **A CAPACITOR IN SERIES BLOCKS DC part of a signal**. However, alternating or changing current can flow through a capacitor. The best analogy is the flow of air from inside to outside of the building. Assuming that the window is completely sealed, air inside the building cannot flow to the outside in spite of the pressure difference between the two sides. The pressure difference is analogous to the voltage potential at the two end of the capacitor. The air flow is like DC current.

However, if the air pressure difference is alternating, there can be air movement on both sides as shown in the diagram.



Connecting two capacitors in parallel results in their capacitances ADD-ed together (just like resistors in series).

Connecting two capacitors in series results in their capacitances combined in a product/sum manner, similar to two parallel resistances.



Before we embark on circuits using capacitors, let us examine one of the signals that you explored in Lab 1 in the past two weeks – the exponential signal.

Exponential signals are interesting. Here the rate of change is shown in terms of the time constant t (tau).

The following facts are worth remembering:

- 1. For exponential rise, the signal reaches 63% at one t, and 95% at 3t.
- 2. For exponential fall, the signal reaches 37% at one t, and 5% at 3t.



For the circuit shown here, assume the capacitor has zero charge (and 0v) at t = 0. The switch is closed, connecting the circuit to the constant voltage source Vs. Initially the voltage drop across the resistor is Vs. A current of Vs/R flows from the source to capacitor. However, a Vc increases, the current I decreases. This results in the exponential drop of changing current and an exponential rise of the capacitor voltage. We will examine mathematically how i and Vc changes over time in the next lecture.

For now, it is important to consider the parameter known as the time constant. If R is large, the charging current I is small, and it takes longer to charge the capacitor. For a given R, if C is large, it can store more charge for a given voltage, therefore the time needed to charge a capacitor to a certain voltage is proportional to the produce R x C. RC is known as the time constant of this circuit.



We have seen this circuit before in the last lecture. We will now derive the exponential equation formally. For that you need to be familiar with solving first-order differential equations from your maths lectures.

We want to solve:

$$\frac{dv}{dt} = \frac{V - v}{RC}$$

$$\frac{dt}{RC} = \frac{dv}{V - v}$$

Integrate both sides, we get:

$$\frac{t}{RC} = -\ln(V - v) + A$$
, where A constant of integration

Use boundary condition: when t=0, v=0:

$$\frac{0}{RC} = -\ln(V - 0) + A$$
$$\Rightarrow A = \ln V$$

Therefor

re

$$\frac{t}{RC} = -\ln(V - v) + \ln V = \ln \frac{V}{V - v}$$

$$\Rightarrow e^{\frac{t}{RC}} = \frac{V}{V - v} \Rightarrow v = V(1 - e^{-\frac{t}{RC}})$$



Let us now consider what happens if we charge up the capacitor, then at t = 0, discharges it. The equations is also easy to solve and it is clear that the discharge profiles in V and I also follow the exponential curves.



We will now consider some common use of capacitors in electronic circuit. The first application of a capacitor is to remove or "block" DC part of an electrical signal.

Consider the circuit above. The input signal Vin has a 3V DC voltage on which is a sinewave. By choosing the correct value for C and R, you can obtain an output signal, Vout, which is has 0V DC (i.e. DC is blocked) and only the sinusoidal signal remains.

The capacitor is acting like a window of an airplane. The constant pressure on outside the airplane (DC value) is not affecting the pressure inside the cabin. However, vibration of the window (sinusoidal component) can pass through to the cabin if the window is sufficient flexible relative to the "resistance" of the cabin air.



Let us take another view of this circuit besides its DC blocking (or AC coupling) quality.

If C= 0.1uF and R = $10k\Omega$, you will find that the circuit will strongly suppress a 5Hz sinewave, but if the input signal has a much higher frequency (say 100 Hz), the signal is passed through the capacitor with little or no reduction.

If you plot signal gain (or attenuation), i.e. Vout/Vin, vs Frequency, you can a characteristic as shown in the slide. The low the Y-axis value, the lower the gain and stronger the suppression. In fact if you project the plot toward frequency = 0 (that is, to DC), the gain is –infinity. That's why this circuit can "BLOCK" DC signal.

Note that the plot here has both axes in logarithmic scale. The frequency axis is clearly log scale. How about the y-axis? It is plotting the gain of the circuit in dB or decibel. dB is also in log scale as will be shown in the next slide.

Decibel

• Ratio of output to input voltage in an electronic system is called voltage gain:

$$A = \frac{V_{out}}{V_{in}}$$

- If the gain is low than 1, we also call this attenuation.
- Voltage gain of a circuit is often expressed in logarithmic form:

$$A_{dB} = 20 \log(\left|\frac{V_{out}}{V_{in}}\right|) = 20 \log A$$

 Power gain of a circuit is the ratio of output power to input power, and is also often expressed in dB, but the equation is different:

Power Gain in dB
$$G_{dB} = 10 \log \left(\frac{P_{in}}{P_{out}}\right) = 10 \log \left(\frac{V_{out}^2}{V_{in}^2}\right)$$

= $20 \log \left(\left|\frac{V_{out}}{V_{in}}\right|\right) = 20 \log A$ P204-205
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In electronics, we often ask the question: what is the ratio of the output to input signal? This ratio is important. If it is larger than 1, the electronic circuit provides gain (we also call this amplification as will be seen in a later lecture).

If the ratio is lower than 1, then the circuit provides attenuation (or suppression).

However, we often express this ratio or gain not just as such a ratio, but in logarithmic scale. Why? It turns out that expressing such ratio in log scale provide us with much higher **dynamic range**. For example, our human perception is generally in log scale, not linear scale. Our hearing and seeing sensitivity is not linear, but logarithmic.

Log - base 10 of the ratio is known as a bel. Scaling this further up by a factor of 10 is known as decibel. That ratio is generally considering the ratio of output power to input power (not voltage). However, since power is voltage square, we found that the common equation to find voltage gain (ratio) in decibel is given by the equation:

$$A_{dB} = 20 \log(\left|\frac{V_{out}}{V_{in}}\right|) = 20 \log A$$

This is an equation that you must commit to memory – very useful for many things!



There are many different types of capacitors depending on the method of construction and the materials used. The most common three types are: polystyrene, ceramic and electrolytic. You will choose which to use depending on the operating signal frequency. Polystyrene and ceramic capacitors are good for a wide frequency range, particularly at very high frequencies (say over 1MHz). However, they only come in fairly low capacitance value.

For large capacitances, one would use electrolytic capacitors. Electrolytic capacitors are only good for fairly low frequencies. Furthermore, they have polarity, i.e. it has a positive and a negative terminal. You must connect the capacitor +ve terminal to the more positive voltage than the –ve terminal.



Inductors are the complementary component to the capacitor. They are not commonly found in electronic circuits because they are bulky and expensive, and practical inductors are far from ideal. However, they are found in motors, transformers and other electrical mechanisms. They are also found as stray effects (undesirable side effects) with interconnecting wires (such as wires that you use to connect circuits on the breadboard). Inductors are used as antennae for sending and receiving radio signals, and form part of transformers used in wireless charging.

Here are some basic equations governing an inductor. The most important is v = IL di/dt. Note the similarity to the capacitor equations.



Series and parallel inductors combine just like resistors do.



Energising an inductor is similar to that of charging a capacitor. Except that the inductor CURRENT (as suppose to the capacitor voltage) is rising exponentially. The inductor voltage is decreasing exponentially.

The time constant is L/R as suppose to RC.



We can perform the same analysis with an inductor being energised (we don't call this charging). At t=0, when the switch is first closed, NO CURRENT FLOWS, since the current through an inductor cannot change instantaneously. Since no current flows, voltage across the inductor must be V, the same as the voltage source. Therefore as soon as the switch is closed, v goes from 0 to V instantaneously! This is a characteristic of a LR circuit.

The current rises from 0, therefore the voltage drop across the resistor R increases, decreasing the inductor voltage. Solving the first-order differential equation provides the exact equations for i_L and v_L .



Similarly when we de-energise the inductor, we get the exponential characteristics as we did for discharging the capacitor.



The take-home message that you must remember is that:

Capacitor tries to keep its voltage constant. Inductor tries to keep its current constant.

